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4	Inventor: Title: Assignee:	Interconnect Structures Micron Technology, Inc.	dlines, Transistor Cate, and Conductive ine, Transistor Gate, and Conductive	10525 U.S. 109/332271 M.J. Nobel
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	11 12 13 14 Date:	6///99	Attorney: David G. Latwesen, Ph. I. Reg. No. 38,533	
	15 16 17 Dat 18 19 20	re:	Inventor: Klaus Florian Schuegra	ut .
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Inventor: Randhir P. S. Thakur

Sheet 1 of 1 Form PTO-1449 U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. MI22-532 SERIAL NO. PATENT AND TRADEMARK OFFICE Unknown LIST OF ART CITED BY APPLICANT APPLICANT Klaus Florian Schuegraf et al. (Use several sheets if necessary) FILING DATE GROUP Unknown U.S. PATENT DOCUMENTS Date Class Subclass *Examiner Document Name Filing Date Number If Appropriate 5,425,392 06/95 Thakur et al. ABAC AB ΑF AC ΑĦ ΑI ΑJ ΑK FOREIGN PATENT DOCUMENTS Document Date Class Subclass Country Translation NumberYes No OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) ΑK Taishi Kubota et al.; "The Effect of the Floating Gate/Tunnel SiO, Interface on FLASH Memory Data Retention Reliability"; 1994; 2 pages ALShoue Jen Wang et al.; "Effects of Poly Depletion on the Estimate of Thin Dielectric Lifetime"; IEEE Electron Device Letters, Vol. 12, No. 11, November 1991; pp. 617-619 AM Klaus F. Schuegraf et al.; "Impact of Polysilicon Depletion in Thin Oxide MOS Technology"; 1993; pp. 86-88

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

E. H. Snow et al.; "Polarization Phenomena and Other Properties of Phosphosilicate Glass Films on Silicon"; Journal of the Electrochemical

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